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(54) DRIVE CIRCUIT FOR INVERTER FOR MOTOR DRIVE

(57) Abstract:

PROBLEM TO BE SOLVED: To increase a speed of ON/OFF operation by a power switching element and stabilize the operation during OFF, by providing a parallel circuit comprising a capacitor and a resistor for connecting an output of a switching element to a high potential end of a DC power for the power switching element.

SOLUTION: Each collector of transistors 15 to 17 is connected to a high potential end of +36 V similar to the source of FET at high side through resistors 18 and 19, resistors 20 and 21, and resistors 22 and 23 connected in series. Capacitors 24, 25 and 26, are connected in parallel to resistors 19, 21 and 23, and its capacitance is 0.01 μm for 5.6 $k\Omega$ resistors 19, 21, and 23. Each base of transistors 15 to 17 is connected to +12 V DC power, and its potential is always kept to +12 V. Each gate of FET at high side is connected respectively to the connecting point between resistors 18 and 19, between resistors 20 and 21, and between resistors 22 and 23 corresponding to each phase.

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